

Low Quiescent Current, PFM/PWM Synchronous Boost Converter

ZS9113T6

■ INTRODUCTION:

The ZS9113T6 is a compact, high-efficiency, fixed frequency, synchronous step-up DC-DC converter. This device provides an easyto-use power supply solution for applications powered by either one-cell, two-cell or three-cell alkaline, NiCd, NiMH, one-cell Li-Ion or Li-Polymer batteries. A low-voltage technology allows the regulator to start up without high inrush current or output voltage overshoot from a low voltage input. High efficiency is accomplished by integrating the low-resistance N-Channel boost switch and synchronous P-Channel switch. ΑII compensation and protection circuitry are integrated to minimize external components. ZS9113T6 consumes less than 14 µA from battery, while operating at no load (Vout = 3.3V, V_{IN} = 1.5V). The devices disconnect provide a true from output (ZS9113T6A) or an input-tooutput bypass (ZS9113T6B), shutdown (EN = GND) state. Both options consume less than 0.6 µA from battery. Output voltage is set by a small external resistor divider.

PIN CONFIGURATION





DEVICE INFORMATION:

PART NUMBER	PACKAGE
ZS9113T6	SOT-23-6L

FEATURES:

(1) Up to 96% Typical Efficiency

1.0A Typical Peak Input Current Limit: IOUT > 200mA@VouT=3.3V, VIN=1.2V IOUT > 400mA@VouT=3.3V, VIN=2.4V IOUT > 400mA@VouT=5.0V, VIN=3.3V

Low Device Quiescent Current: -Output Quiescent Current: < 4 µAtypical, device is not switching (Vout > Vin, excluding feedback divider current)

-Input Sleep Current: 1 μA

-No Load Input Current: 14 µAtypical

Shutdown Current: 0.6 µAtypical €

1 Low Start-up Voltage: 0.82V, 1 mA load

(1) Low Operating Input Voltage: down to 0.65V

1 Adjustable Output Voltage Range: 1.8V to 5.5V

(D) Maximum Input Voltage: Vouт < 5.5V

Automatic PFM/PWM Operation: **(1)**

-PWM Operation: 500 KHz

-PFM Output Ripple: 150 mV typical

Feedback Voltage: 1.215V **(1)**

Inrush Current Limiting and Internal Soft Start (1 ms typical)

Selectable, Logic Controlled, Shutdown States: -True Load Disconnect Option (ZS9113T6A)

-Input to Output Bypass Option (ZS9113T6B)

Over temperature Protection

Output Short Protection **(1)**

APPLICATIONS:

One, Two and Three Cell Alkaline and NiMH/NiCd Portable Products

Solar Cell Applications **(1)**

Personal Care and Medical Products **a**

(1) Bias for Status LEDs

1 Smartphones, MP3 Players, Digital Cameras

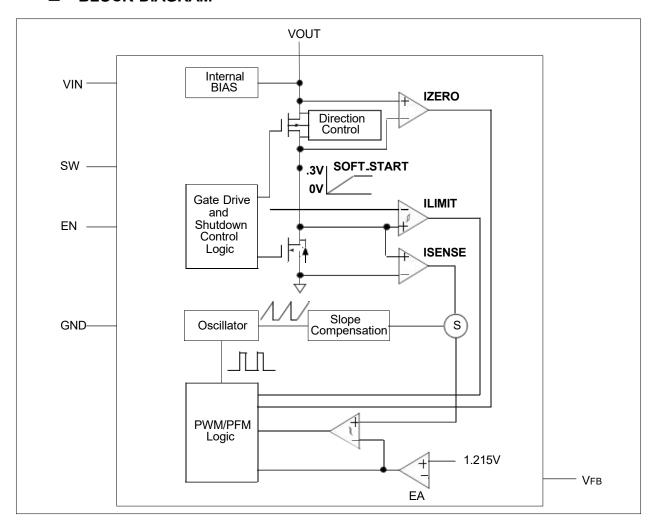
(P) Remote controllers, Portable Instruments

1 Wireless Sensors

(1) Bluetooth Headsets

(+3.3V to +5.0V Distributed Power Supply

■ BLOCK DIAGRAM

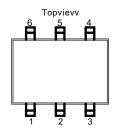


ZS9113T6 Block Diagram

■ ORDER INFORMATION

PART NUMBER	SHUTDOWN STATE
ZS9113T6A	True Load Disconnect
ZS9113T6B	Input to Output Bypass

■ PIN CONFIGURATION



PIN NO.	PIN NAME	FUNCTION		
1	SW	Switch Node, Boost Inductor Input Pin		
2	GND	Ground Pin		
3	EN	Enable Control Input Pin		
4	V_{FB}	Feedback Voltage Pin		
5	V _{OUT}	Output Voltage Pin		
6	V _{IN}	Input Voltage Pin		

■ ABSOLUTE MAXIMUM RATINGS⁽¹⁾ (Unless otherwise specified, T_A=25°C)

PARAMETI	ER	SYMBOL	RATINGS	UNITS
Input Voltag	e ⁽²⁾	V_{IN}	-0.3~ 6	V
SW Voltag	e ⁽²⁾		-0.3~ 6	V
CE,FB Volta	age ⁽²⁾		-0.3~ 6	V
V _{OUT} Voltage	e ⁽²⁾		-0.3~6	V
Ourput Current Byp	ass Mode		1000	mA
Power Dissipation SOT23-6L		P _D	400	mW
Operating Virtual Temperature F	1	T _A	-40~+85	$^{\circ}$
Storage Temperatu	ıre Range	T _{stg}	-40~+125	$^{\circ}$ C
Lead Temperature (Soldering, 10 sec)		T_{solder}	260	$^{\circ}$ C
ESD rating		Human Body Model (HBM)	4000	V
		Machine Model (MM)	400	V

⁽¹⁾ Stresses beyond those listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under recommended operating conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods my affect device reliability.

■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	MIN.	NOM.	MAX.	UNITS
Supply voltage at V _{IN}	-0.3	-	6	V
Output voltage at V _{OUT}	-0.3	-	6	V
Operating free air temperature range, T_A	-40	_	85	${\mathbb C}$

⁽²⁾ All voltages are with respect to network ground terminal.

Electrical Characteristics

■ ELETRICAL CHARACTERISTICS

Vin=1.5V, Cout=Cin=10 μ F, L=4.7 μ H, Vout=3.3V, lout=0mA, Typical values are at T_A=25°C, unless otherwise specified.

PARAMETER	SYMBOL	CONDITIONS		MIN.	TYP. (1)	MAX.	UNITS
Minimum Start-Up Voltage	V _{IN}	I _{LOAD} =1r	mA	-	0.82	-	V
Minimum Operating Voltage ⁽²⁾	V _{IN}	I _{LOAD} =1r	mA	-	0.65	-	V
Input Voltage Range	V_{IN}			0.82		5.5	V
Feedback Voltage	V_{FB}			1.179	1.215	1.251	V
Feedback Input Bias Current	Ivғв			-	10	-	nA
Output Voltage Adjust Range ⁽³⁾	V _{оит}	Vout≥Vin		1.8		5.5	V
Output Voltage	V _{оит}	EN=0V	ZS9113T6A		0		V
Output Voltage			ZS9113T6B		VIN-0.6V		
_		V _{IN} =1.2V,V _{OUT} = 2V			200	-	
Maximum Output Current	Іоит	$V_{IN}=2.4V, V_{OUT}=3.3V$			400	_	mA
Current		V _{IN} =3.3V,V _{OUT} = 5.0V			400	_	
Vоит Quiescent Current ⁽⁴⁾	Ідоит	IOUT=0mA, device is not switching, EN= VIN=4V, VOUT=5V		-	4	8	μA
VIN Sleep Current(5)	I _{QIN}	IOUT=0mA, EN=VIN		-	1	2.3	μΑ
No load Input Current	I _{INO}	I _{OUT} = 0mA, device is switching		-	14	25	μA
Quiescent Current Shutdown	I _{QSHDN}	V _{OUT} = 3	3.3V	-	0.6	-	μA

⁽¹⁾ Typical numbers are at 25°C and represent the most likely norm.

⁽²⁾ Minimum V_{IN} operation after start-up is only limited by the battery's ability to provide the necessary power as it enters a deeply discharged state.

⁽³⁾ For VIN > VOUT, VOUT will not remain in regulation.

⁽⁴⁾ IQOUT is measured at VOUT, VOUT is external supplied for VOUT > VIN (device is not switching)

⁽⁵⁾ IQIN is measured at VIN pin during Sleep period, no load. Determined by characterization, not production tested.

■ ELETRICAL CHARACTERISTICS

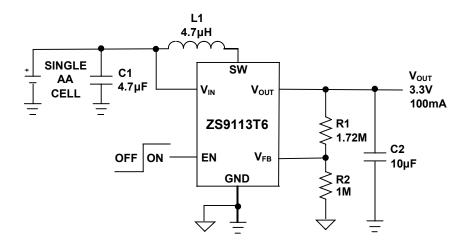
Vin=1.5V, Cout=Cin=10 μ F, L=4.7 μ H, Vout=3.3V, Iout=0mA, Typical values are at T_A=25°C, unless otherwise specified.

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP. (1)	MAX.	UNITS
NMOS Switch Leakage	I _{NLK}	VIN=Vsw=5V, Vout =5.5V, EN=Vfb=GND	-	0.15	-	μA
PMOS Switch Leakage	I _{PLK}	VIN=VSW=GND, VOUT=5.5V	-	0.15	-	μA
NMOS Switch On Resistance	R _{DS(ON)N}	V _{OUT} = 3.3V, Isw=100mA	-	0.25	-	Ω
PMOS Switch On Resistance	R _{DS(ON)P}	V _{OUT} = 3.3V Isw=100mA	-	0.5	-	Ω
NMOS Peak Switch Current Limit ⁽¹⁾	I _{N(MAX)}		-	1	-	Α
Vоит Accuracy ⁽²⁾	V _{OUT} %	V _{IN} =1.5V	-3	-	+3	%
Switching Frequency	fsw			500		KHz
EN Input Logic High	ViH	Iouт=1mA	70	-	-	% of V _{IN}
EN Input Logic Low	VIL	Iouт=1mA	ı	-	20	% of Vin
EN Input Leakage Current	I ENLK	V _{EN} =5V	-	5	-	nA
Line Regulation	V_{OUT} ΔV_{OUT}	1.5V≤V _{IN} ≤2.8V Ιουτ=50mA	-0.4	0.3	0.4	%/V
Load Regulation	$\frac{\Delta V_{OUT}}{V_{OUT}}$	25mA≤louт=≤100mA V _{IN} =1.5V	-1.5	0.1	1.5	%
Maximum Duty Cycle(1)	DC _{MAX}		87	89	91	%
Soft Start Time(1)	t _{ss}	EN Low to High 90% of Vout	-	1	-	ms
Thermal Shutdown Die Temperature	T_{SD}	I _{OUT} ==20mA V _{IN} =1.4V	-	160	-	°C
Die Temperature Hysteresis	T _{SDHY}		-	20	-	$^{\circ}$ C

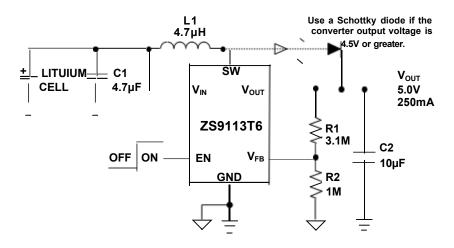
⁽¹⁾ Determined by characterization, not production tested.

⁽²⁾ Includes Line and Load Regulation

■ TYPICAL APPLICATION CIRCUITS



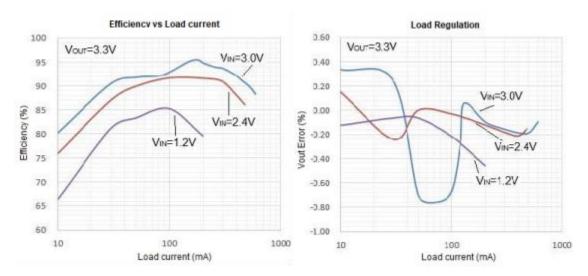
Circuit 1

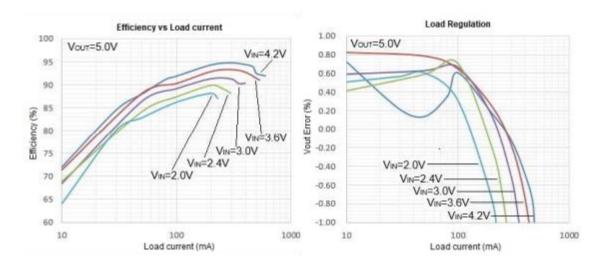


Circuit 2

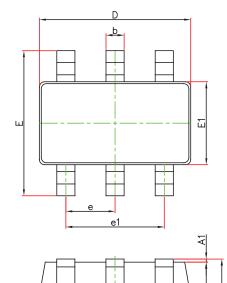
Typical Characteristics

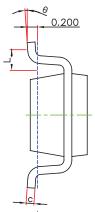
(T_A=25℃, unless otherwise specified)





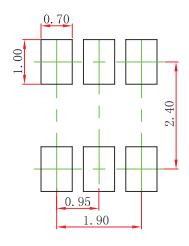
SOT-23-6L Package Outline Dimensions





Symbol	Dimensions	s In Millimeters	Dimensions In Inch		
Зупион	Min Max		Min	Max	
Α	1.050	1.250	0.041	0.049	
A 1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	2.650	2.950	0.104	0.116	
E1	1.500	1.700	0.059	0.067	
е	0.950	50(BSC) 0.037(BSC		(BSC)	
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

SOT-23-6L Suggested pad Layout



Note:

- 1.Controlling dimension: in millimeters.
 2.General tolerance: ± 0.05mm.
 3.The pad layout is for reference purposes only.

DISCLAIMER

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